

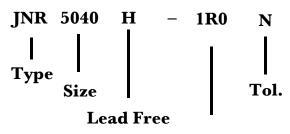
SMD POWER COIL-JNR 5040H



FEATURES

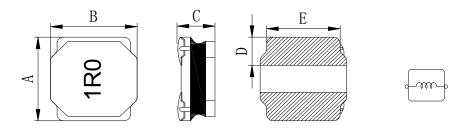
- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
 3. Operating temperature :-40~+125°C (Including self temperature rise)

PRODUCT IDENTIFICATION



Inductance

DIMENSIONS (mm)

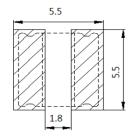


Part No.	Inductance			Size (mm)		
Part NO.		Α	В	С	D	Е
JNR 5040H	\leq 10 uH	4.95 ± 0.2	4.95 ± 0.2	3.9 ± 0.2	1.3 ± 0.3	4.2 ± 0.2
JNK 5040H	>10 uH			3.8 ± 0.2		

* Dimensions are not including the termination.

For maximum overall dimensions with termination, add 0.1mm.

Recommended PC Board Pattern



Note:

1.PCB layout is referred to standard IPC-7351B

- 2. The above PCB layout reference only.
- 3. Recommend solder paste thickness at 0.12mm and above.

SERIES LIST

		L	Tol.	RDC	Isat	Irms
No.	Part No.			(m Ω)	(A)	(A)
		(μH)		±20%		
1	JNR 5040H-R47N	0.47	±30%	6.5	12.0	9.00
2	JNR 5040H-R60N	0.60	±30%	8	11.0	8.00
3	JNR 5040H-1R0M	1.0	±20%	12	7.50	5.00
4	JNR 5040H-1R5M	1.5	±20%	15	6.50	4.50
5	JNR 5040H-1R8M	1.8	±20%	18	6.10	4.20
6	JNR 5040H-2R2M	2.2	±20%	21	5.70	3.80
7	JNR 5040H-3R3M	3.3	±20%	24	4.40	3.50
8	JNR 5040H-4R7M	4.7	±20%	32	3.90	3.20
9	JNR 5040H-6R8M	6.8	±20%	43	3.30	2.50
10	JNR 5040H-8R2M	8.2	±20%	50	2.90	2.35
11	JNR 5040H-100M	10	±20%	56	2.52	2.20
12	JNR 5040H-150M	15	±20%	80	2.00	1.80
13	JNR 5040H-220M	22	±20%	123	1.62	1.50
14	JNR 5040H-270M	27	±20%	160	1.40	1.30
15	JNR 5040H-330M	33	±20%	180	1.30	1.20
16	JNR 5040H-470M	47	±20%	270	1.10	1.00
17	JNR 5040H-680M	68	±20%	400	0.90	0.80
18	JNR 5040H-820M	82	±20%	490	0.78	0.75
19	JNR 5040H-101M	100	±20%	560	0.75	0.72
20	JNR 5040H-221M	220	±20%	1500	0.62	0.55

Note:

1. Test Frequency : 100KHz /1V

2. All test data referenced to $25^\circ\!\mathrm{C}$ ambient

3. Isat : Saturation Current (Isat) will cause L0 to drop approximately 30%.

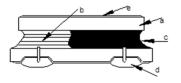
4. Irms : Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of $40^\circ\!\mathrm{C}$

5. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

6. Special inquiries besides the above common used types can be met on your requirement.

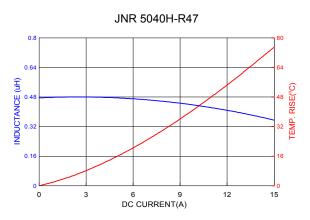
7. Rated DC current: The lower value of Irms and Isat.

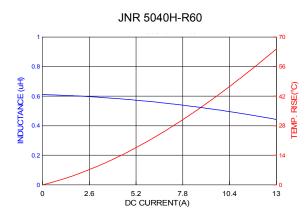
Materials

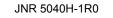


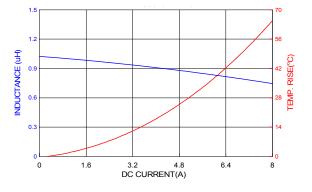
No.	Description	Specification	
а	Core	Ferrite Core	
b	Wire	Enameled Copper Wire	
С	Glue	Epoxy with magnetic powder	
d	Terminal	Ag/Ni/Sn+ Sn Solder	
е	Ink	Halogen-free ketone	

TYPICAL PERFORMANCE CURVES

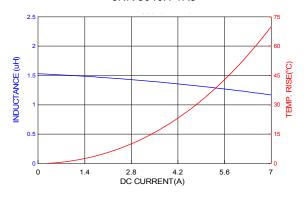




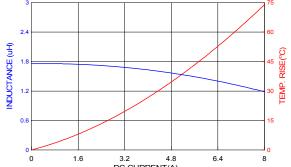


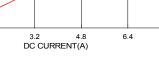


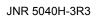
JNR 5040H-1R5

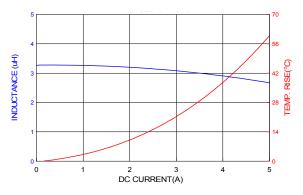


JNR 5040H-1R8

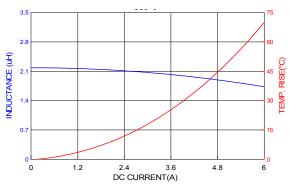




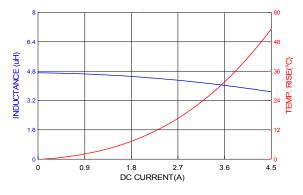


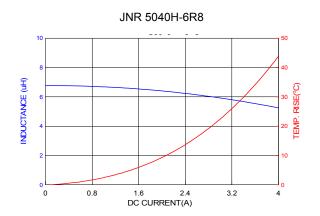


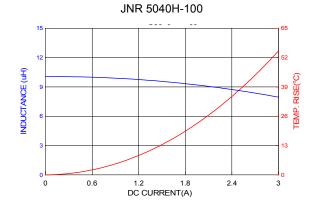
JNR 5040H-2R2

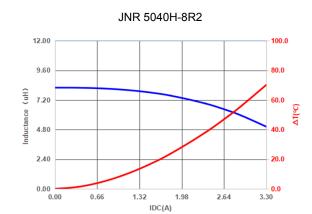


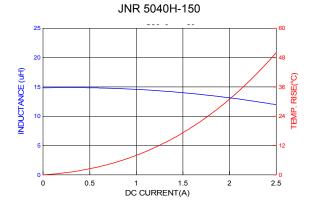
JNR 5040H-4R7

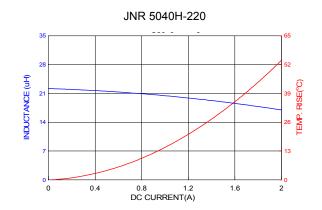


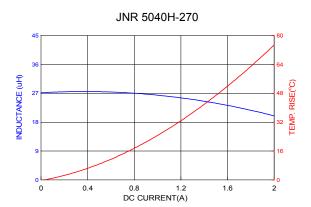


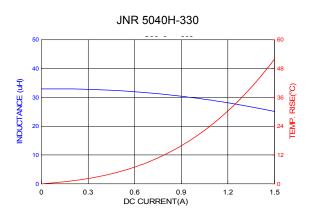




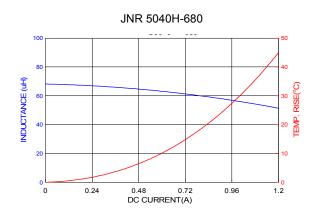


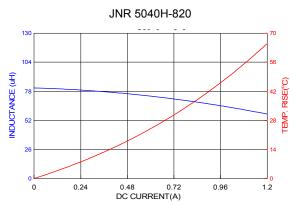




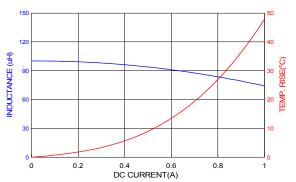




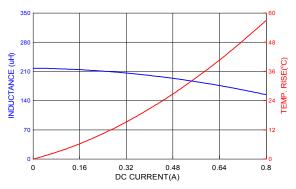












Appearance criterion

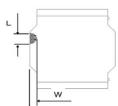
1 . Core chipping

The appearance standard of the chipping size on top side, and bottom side ferrite core is listed below. Chip off is generated during molding and manufacturing process.

Chip off acceptance limits subjected to the product size.

Our current Defect limit is based on the IPC-A-610.

Some chip off does not impact the product function, see the IPC standard 1 & 2.

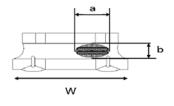


L	\leq 50 % of the length
W	\leq 25 % of the width

Defects usually occur at the corners and edges of the product, There will be a slight defect black and rough, but not exposed copper, and does not affect the product performance and reliability.

2 Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.

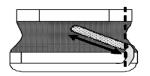


Exposed wire tolerance limit of coating resin part on product side. Size of exposed wire occurring to coating resin is specified below.

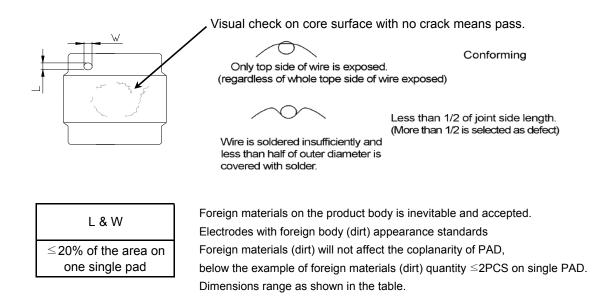
- 1. Width direction (dimension a) : Acceptable when $a \leq w/2$.
- 2. Length direction (dimension b) : Dimension b is not specified.
- 3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

3 . External appearance criterion for exposed wire

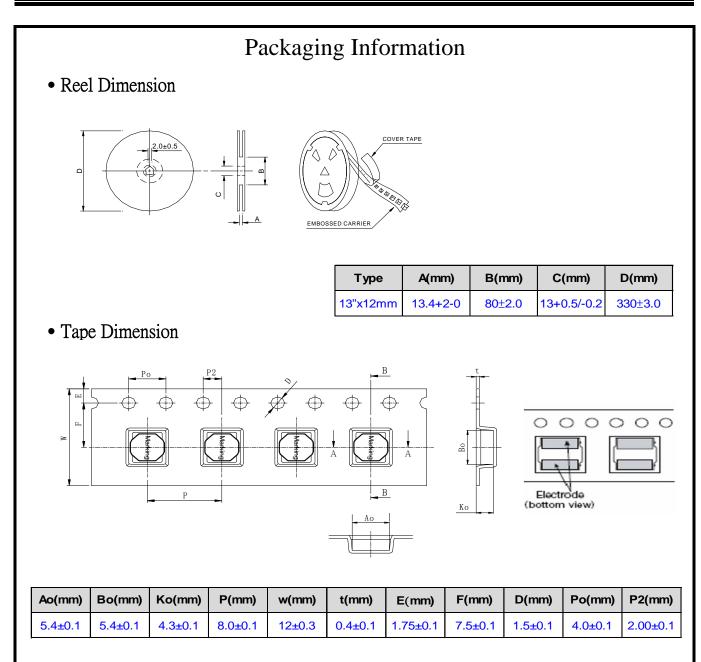
Exposed winding wire at the secondary side is regarded as qualified product.



4、 Electrode appearance criterion for exposed wire



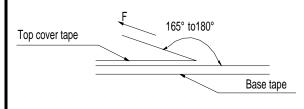




• Packaging Quantity

Size	Reel	
JNR 5040H	1500	

• Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions (referenced ANSI/EIA-481-D-2008 of 4.11 standard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(°C)	(%)	(hPa)
300±10%	5~35	45~85	860~1060

Application Notice

· Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

- 1. Products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ\!\mathrm{C}$ and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.