



PRODUCT IDENTIFICATION

JRPI	1005	PV	- 3R3	M
Type	Size	Control	Inductance	
				Tol.

FEATURES

1. Shielded construction.
2. Capable of corresponding high frequency.
3. Low loss realized with low DCR.
4. High performance (Isat) realized by metal dust core.
5. Ultra low buzz noise, due to composite construction.
6. 100% Lead(Pb)-Free and RoHS compliant.
7. High reliability -Reliability test complied to AEC-Q200
8. Operating temperature -55~+155°C
(Including self - temperature rise)

Marking and Date code:

Marking ex:3.3(μH) → 3R3

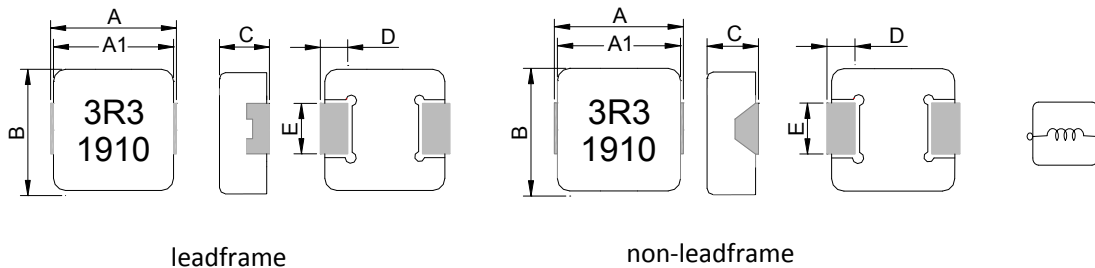
Date code

19 10 → year and weekly

APPLICATIONS

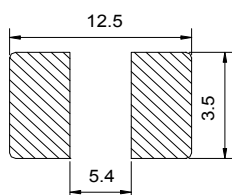
1. DC/DC converters in distributed power systems.
2. DC/DC converter for Field Programmable Gate Array(FPGA).
3. Battery powered devices.
4. Thin type on-board power supply module for exchanger.
5. VRM for server.
6. High current, low profile POL converters.
7. PDA/notebook/desktop/server and battery powered devices.

DIMENSIONS (mm)



印字:黑色3R3及D/C 1910 (19年,10週期)(依實際生產日期而定)

Recommend PC Board Pattern



Note:

1. PCB layout is referred to standard IPC-7351B
2. The above PCB layout reference only.
3. Recommend solder paste thickness at 0.15mm and above

Part No.	Size (mm)					
	A	A1	B	C	D	E
JRPI 1005PV	11.0 ± 0.5	10.0 ± 0.5	10.0 ± 0.3	4.8 ± 0.2	2.0 ± 0.3	See Spec table

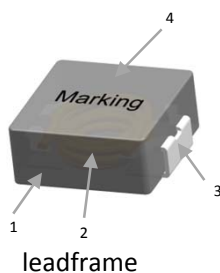
SERIES LIST

No.	Part No.	L (μ H) $\pm 20\%$	RDC (m Ω)		Isat (A)		Irms (A)		E (mm) ± 0.3	Type
			Typ.	Max.	Typ.	Max.	Typ.	Max.		
1	JRPI 1005PV-R36M	0.36	0.82	0.92	52.0	46.0	34.0	30.0	3.0	non-leadframe
2	JRPI 1005PV-R47M	0.47	1.15	1.32	46.0	40.0	33.0	29.0	3.0	non-leadframe
3	JRPI 1005PV-R68M	0.68	1.60	1.90	35.0	32.0	28.0	25.0	2.5	non-leadframe
4	JRPI 1005PV-1R0M	1.00	2.60	3.00	33.0	30.0	25.0	23.0	2.5	non-leadframe
5	JRPI 1005PV-1R5M	1.50	3.40	3.80	27.0	24.0	23.0	21.0	2.5	non-leadframe
6	JRPI 1005PV-2R2M	2.20	5.10	5.60	20.0	18.0	19.5	17.5	3.0	leadframe
7	JRPI 1005PV-3R3M	3.30	8.10	9.10	17.5	15.5	17.0	15.0	3.0	leadframe
8	JRPI 1005PV-4R7M	4.70	9.30	10.5	16.0	14.0	15.0	13.0	3.0	leadframe
9	JRPI 1005PV-5R6M	5.60	12.8	14.4	15.0	12.5	13.0	11.0	3.0	leadframe
10	JRPI 1005PV-6R8M	6.80	15.0	17.3	14.0	12.0	12.0	10.0	3.0	leadframe
11	JRPI 1005PV-100M	10.0	18.9	21.8	13.0	11.0	7.6	7.2	3.0	leadframe
12	JRPI 1005PV-220M	22.0	44.0	54.0	6.0	5.5	6.0	5.5	3.0	leadframe
13	JRPI 1005PV-470M	47.0	106	127	4.0	3.5	4.5	4.0	3.0	leadframe
14	JRPI 1005PV-101M	100.0	242	290	2.8	2.4	2.2	2.0	3.0	leadframe

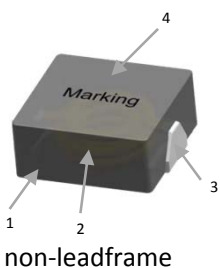
Note:

1. Test Frequency: 100KHz /1V
2. All test data referenced to 25°C ambient
3. Saturation Current (Isat) will cause L0 to drop approximately 30%.
4. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C
5. The part temperature (ambient + temp rise) should not exceed 155°C under worst case operating conditions.
Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
6. Irms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.
7. Rated DC Current : The less value which is Irms or Isat

Materials

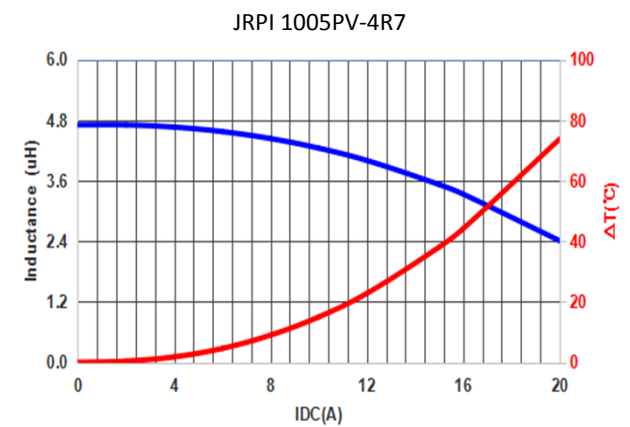
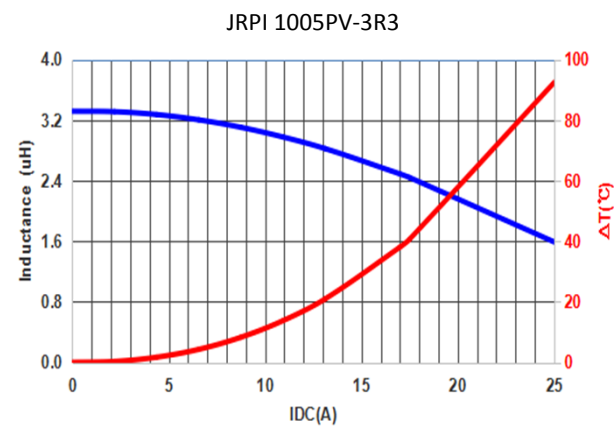
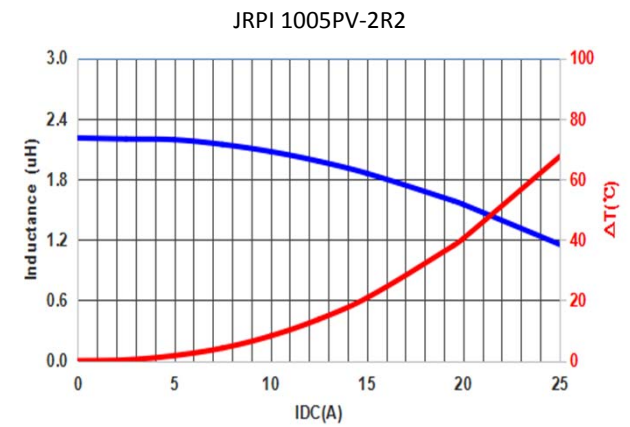
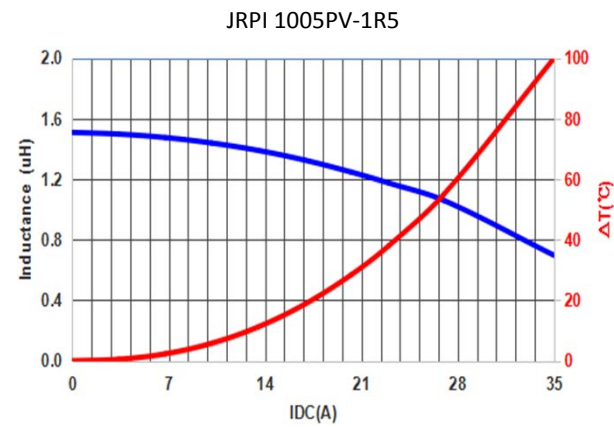
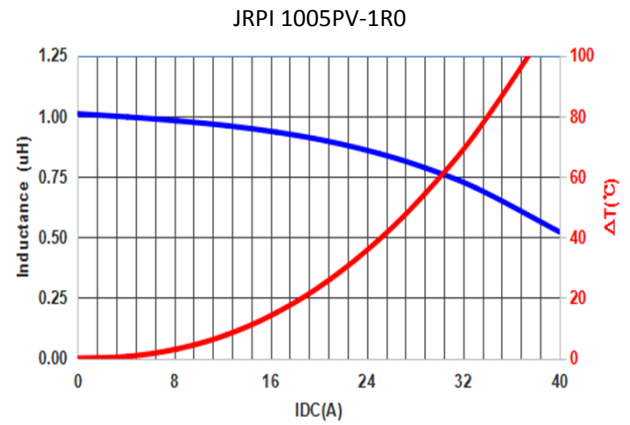
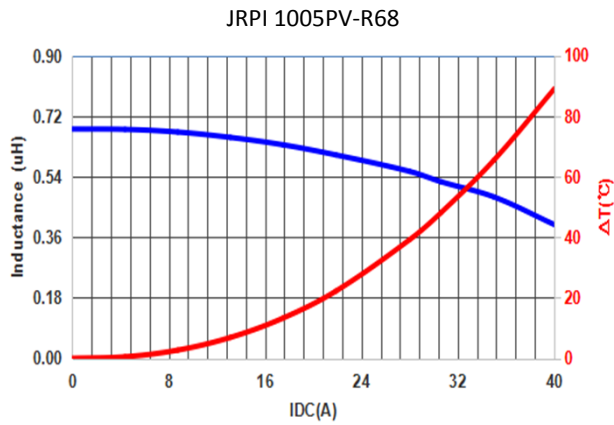
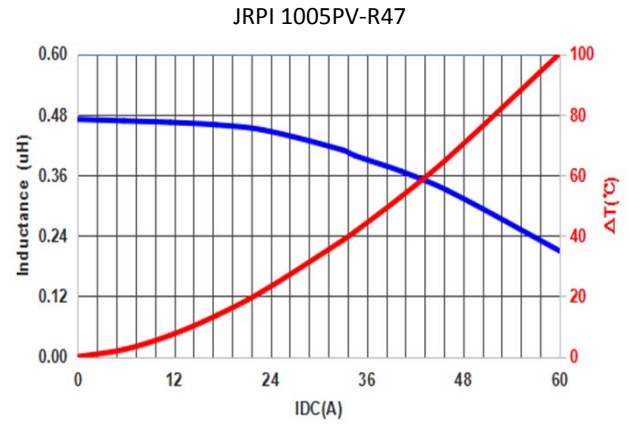
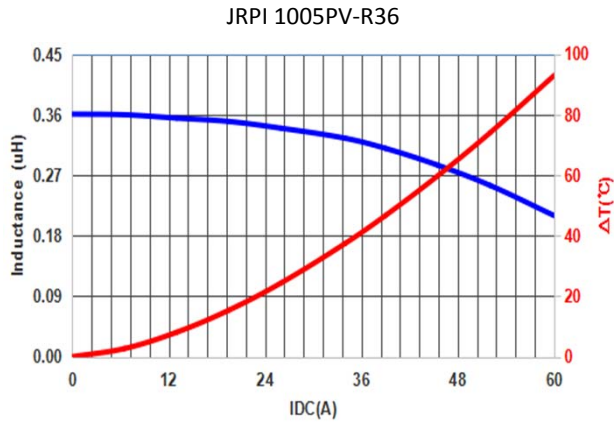


NO	Items	Materials
1	Core	Alloy Powder
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+Sn---Plating)
4	Ink	Halogen-free ketone

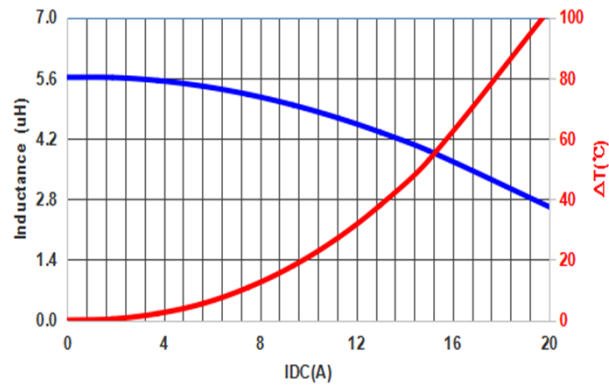


NO	Items	Materials
1	Core	Alloy Powder
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone

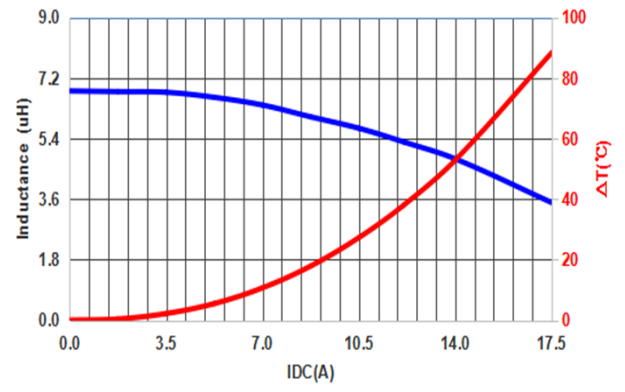
Typical Performance Curves



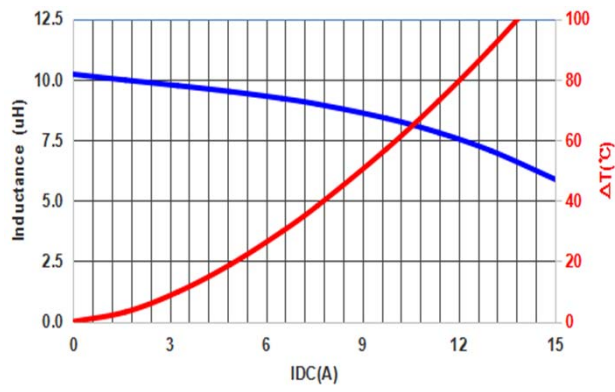
JRPI 1005PV-5R6



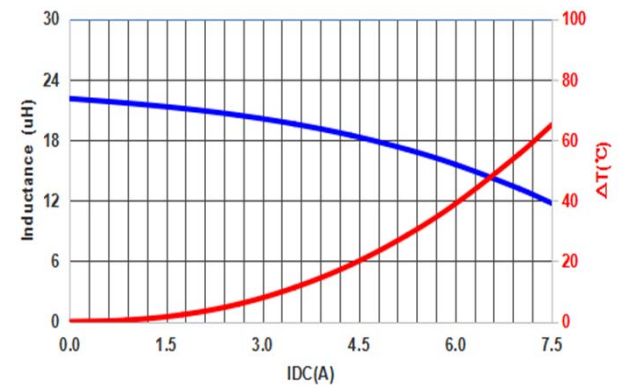
JRPI 1005PV-6R8



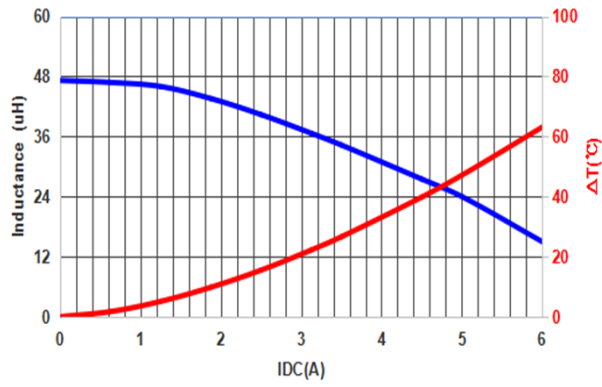
JRPI 1005PV-100



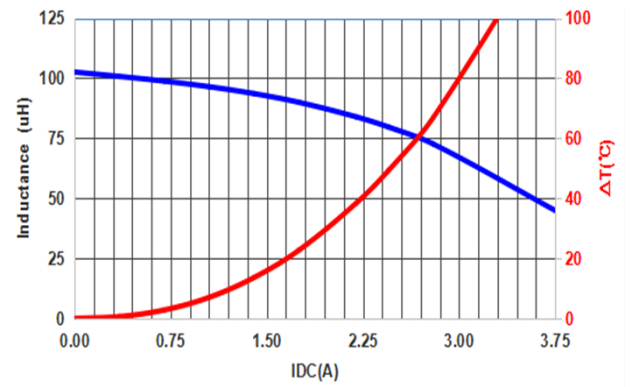
JRPI 1005PV-220



JRPI 1005PV-470

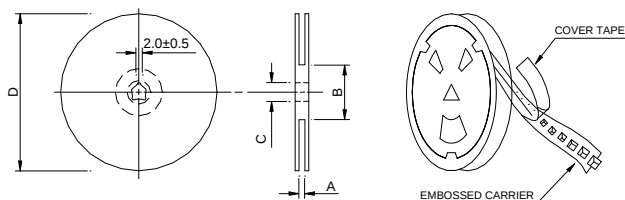


JRPI 1005PV-101



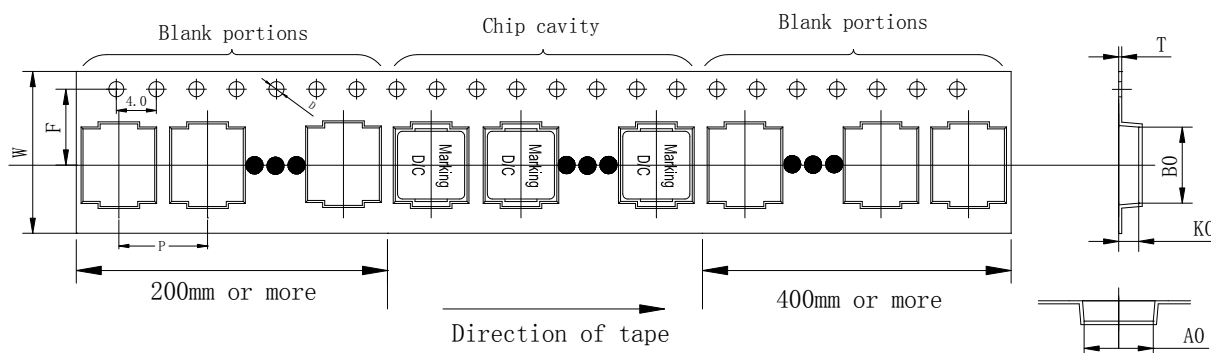
Packaging Information

• Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

• Tape Dimension



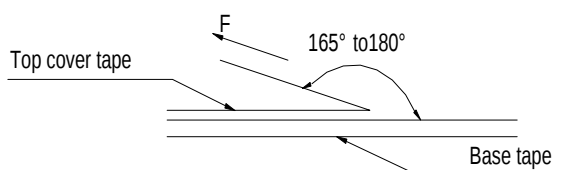
Ao(mm)	Bo(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
10.4±0.1	11.6±0.1	5.3±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.1	1.5±0.1

• Packaging Quantity

Size	Reel	Inner box	Carton
JRPI 1005PV	500	1000	4000

• Tearing Off Force

The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions (referenced ANSI/EIA-481-C-2003 of 4.11 standard)



Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

■ Application Notice

- Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

1. Products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
2. Temperature and humidity conditions: Less than 40°C and 60% RH.
3. Recommended products should be used within 12 months form the time of delivery.
4. The packaging material should be kept where no chlorine or sulfur exists in the air.

- Transportation

1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.