



PRODUCT IDENTIFICATION

JRPI	1707	V	- 4R7	M
Type	Size	Vehicle		Tol.
			Inductance	

FEATURES

1. Carbonyl Powder.
2. Compact design.
3. High current , low DCR , high efficiency.
4. Very low acoustic noise and very low leakage flux noise.
5. High reliability.
6. 100% Lead(Pb)-Free and RoHS compliant.
7. High reliability -Reliability test complied to AEC-Q200.

APPLICATIONS

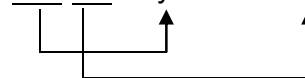
Note PC power system , incl. IMVP-6
DC/DC converter .

Marking and Date code:

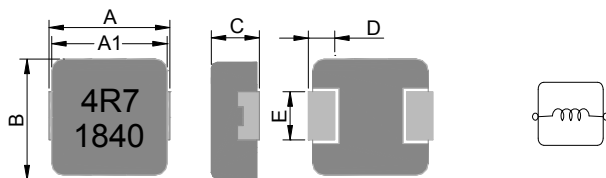
Marking ex:4.7(μH) → 4R7

Date code

18 40 → year and weekly

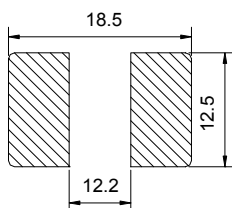


DIMENSIONS (mm)



印字:黑色4R7及D/C 1840 (18年,40週期)(依實際生產日期而定)

Recommend PC Board Pattern



Note:

1. The above PCB layout reference only.
2. Recommend solder paste thickness at 0.17mm and above.

Part No.	Size (mm)					
	A	A1	B	C	D	E
JRPI 1707V	17.6 ± 0.4	16.9 ± 0.3	16.9 ± 0.3	6.7 ± 0.3	2.1 ± 0.3	11.9 ± 0.3

SERIES LIST

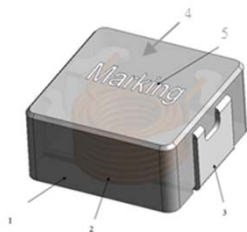
No.	Part No.	L (μH)	Test Frequency (Hz)	RDC (mΩ)		Isat 1 (A)	Isat 2 (A)	Irms (A)
				Typ.	Max.			
1	JRPI 1707V-1R0M	1.00	100K/1V	1.60	2.00	60.0	70.0	52.0
2	JRPI 1707V-1R3M	1.30	100K/1V	1.70	2.30	54.0	67.0	49.0
3	JRPI 1707V-1R5M	1.50	100K/1V	2.00	2.50	52.0	65.0	47.0
4	JRPI 1707V-2R2M	2.20	100K/1V	2.40	2.70	47.0	62.0	43.5
5	JRPI 1707V-3R3M	3.30	100K/1V	3.50	3.90	45.0	54.0	28.0

No.	Part No.	L (μ H)	Test Frequency (Hz)	RDC (m Ω)		Isat 1 (A) Typ.	Isat 2 (A) Typ.	Irms (A) Typ.
				Typ.	Max.			
6	JRPI 1707V-4R7M	4.70	100K/1V	4.80	5.50	41.0	50.0	25.0
7	JRPI 1707V-5R6M	5.60	100K/1V	5.80	7.05	40.0	45.0	21.0
8	JRPI 1707V-6R8M	6.80	100K/1V	8.40	9.20	32.0	39.0	19.0
9	JRPI 1707V-8R2M	8.20	100K/1V	9.60	10.8	25.0	31.0	18.0
10	JRPI 1707V-100M	10.0	100K/1V	11.8	13.0	24.0	29.0	16.5
11	JRPI 1707V-150M	15.0	100K/1V	17.8	20.5	23.0	27.0	12.5
12	JRPI 1707V-220M	22.0	100K/1V	25.1	26.5	18.0	23.0	12.0
13	JRPI 1707V-330M	33.0	100K/1V	38.0	44.0	15.0	20.0	10.7
14	JRPI 1707V-390M	39.0	100K/1V	40.0	48.0	11.0	18.0	9.20
15	JRPI 1707V-470M	47.0	100K/1V	48.0	55.0	9.50	16.0	8.70
16	JRPI 1707V-560M	56.0	100K/1V	54.0	62.0	9.00	15.0	7.80
17	JRPI 1707V-680M	68.0	100K/1V	68.0	80.0	8.00	13.0	7.00
18	JRPI 1707V-820M	82.0	100K/1V	87.0	100.0	7.00	12.0	5.70
19	JRPI 1707V-101M	100.0	100K/1V	102.0	118.0	6.50	12.0	5.30

Note:

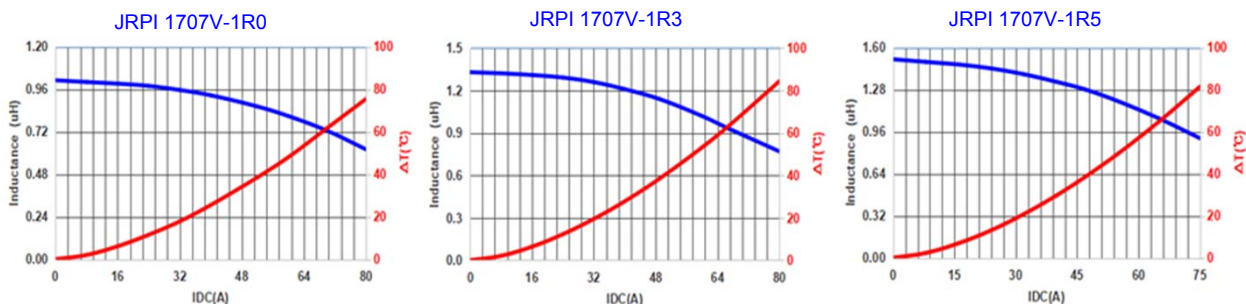
1. All test data referenced to 25°C ambient
2. Saturation Current (Isat1) will cause L0 to drop approximately 20%
Saturation Current (Isat2) will cause L0 to drop approximately 30%.
3. Heat Rated Current (Irms) will cause the coil temperature rise approximately Δt of 40°C
4. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.
Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
5. Special inquiries besides the above common used types can be met on your requirement.

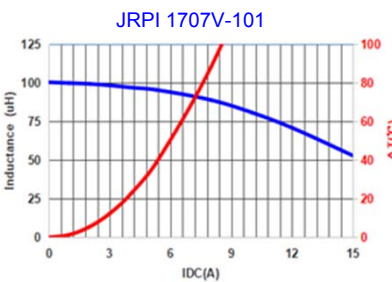
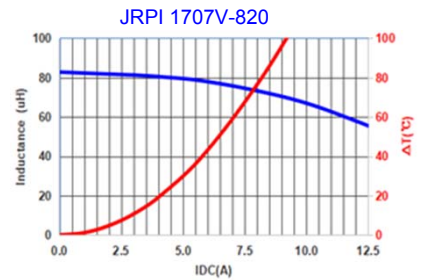
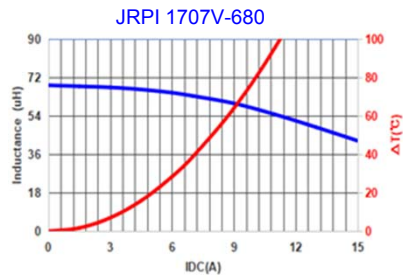
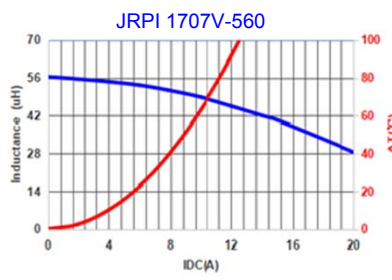
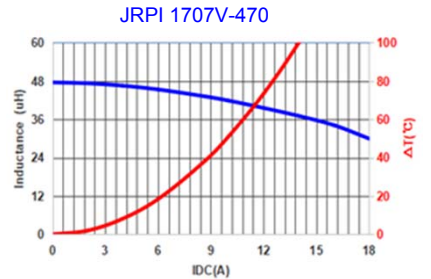
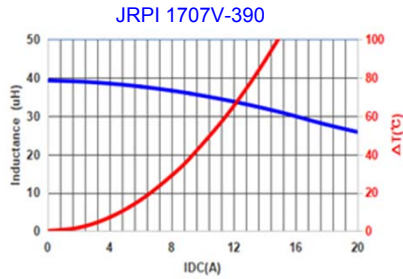
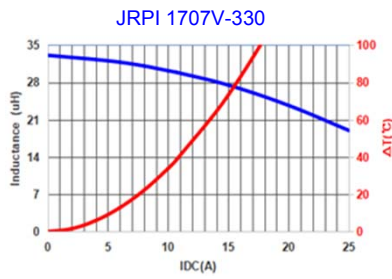
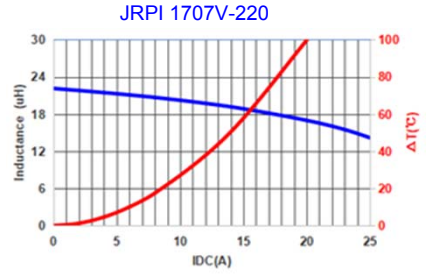
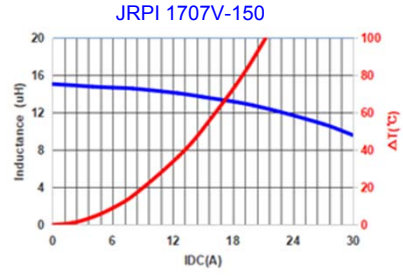
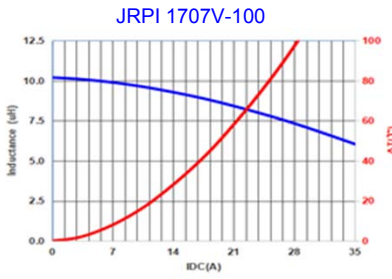
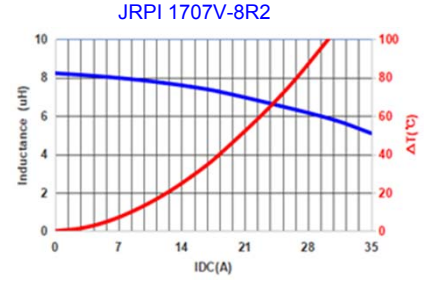
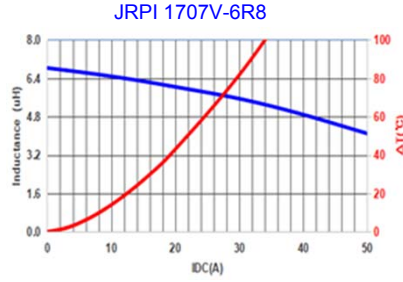
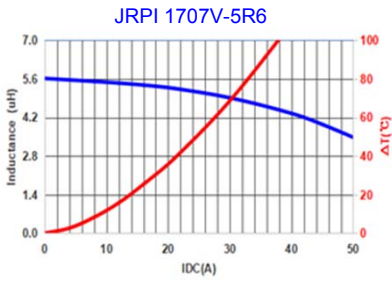
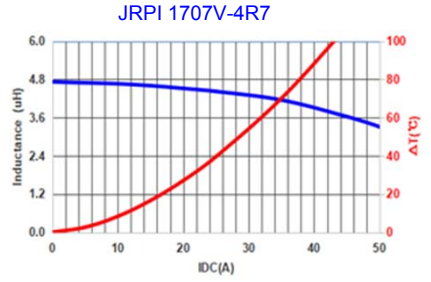
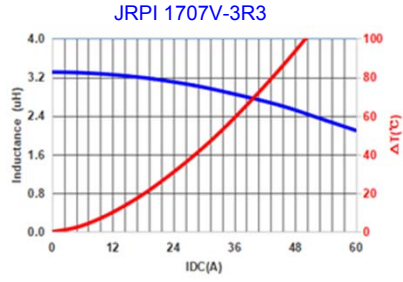
Materials



NO	Items	Materials
1	Core	Carbonyl Powder
2	Wire	Polyester Wire or equivalent
3	Clip	100% Pb free solder(Ni+Sn---Plating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

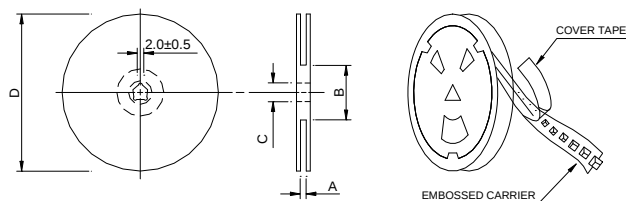
TYPICAL PERFORMANCE CURVES





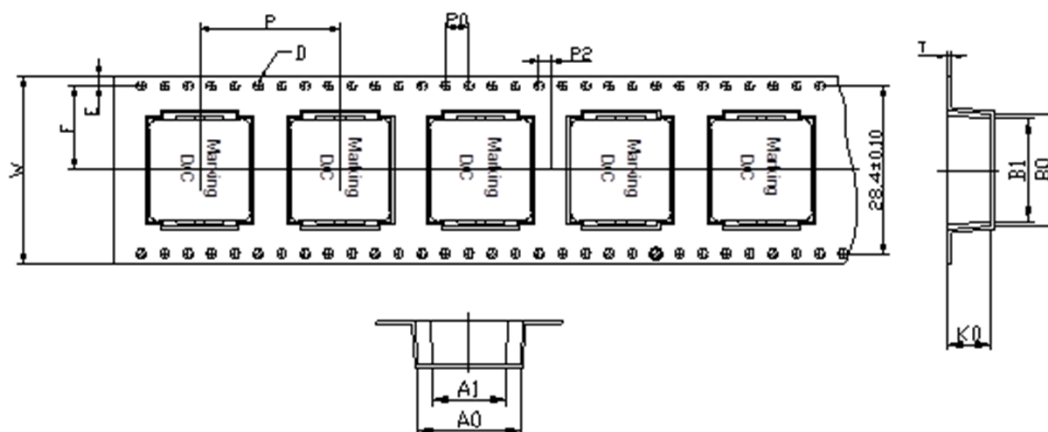
Packaging Information

• Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x32mm	32.4+2/-0	100±2	13+0.5/-0.2	330

• Tape Dimension

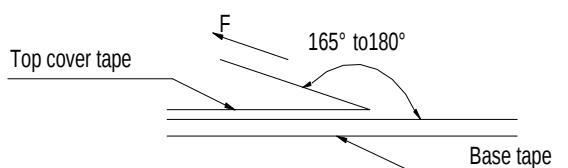


Ao(mm)	A1(mm)	Bo(mm)	B1(mm)	Ko(mm)	P(mm)	P0(mm)	P2(mm)	W(mm)	E(mm)	F(mm)	t(mm)	D(mm)
17.5±0.1	12.6±0.1	18.5±0.1	17.5±0.1	7.5±0.1	24±0.1	4.0±0.1	2.0±0.05	32±0.3	1.75±0.1	14.2±0.1	0.5±0.05	1.5±0.1

• Packaging Quantity

Size	Reel	Inner box	Carton
JRPI 1707V	200	200	800

• Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions (referenced ANSI/EIA-481-C-2003 of 4.11 standard)

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

■ Application Notice

- Storage Conditions(component level)

To maintain the solderability of terminal electrodes:

1. Products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
2. Temperature and humidity conditions: Less than 40°C and 60% RH.
3. Recommended products should be used within 12 months form the time of delivery.
4. The packaging material should be kept where no chlorine or sulfur exists in the air.

- Transportation

1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.